REMARKS

Reconsideration and allowance of the present application are respectfully requested. Claims 1-5, 9-27, and 31-61 are currently pending in this application.

Entry of this Response Under 37 C.F.R. § 1.116 is Appropriate and is Respectfully Requested.

In the present Response, the subject matter of now withdrawn claims 6-8 has been incorporated into independent claim 1. The subject matter of now withdrawn claims 28-30 has been incorporated into independent claim 23. The other independent claims (i.e., claims 44, 52, and 57) have been amended in a related fashion. As the Patent Office has already searched and addressed the subject matter recited in claims 6-8 and 28-30, it is believed that the above amendments do not necessitate a new search, or raise any new considerations. Accordingly, entry of this Response is deemed appropriate and is respectfully requested.

Information Disclosure Statement

An Information Disclosure Statement is submitted herewith. This Information Disclosure Statement lists two references that were cited by the Patent Office in an Office Action dated July 18, 2003 in U.S. Application Serial No. 09/632,662. The Examiner is respectfully requested to indicate consideration of these two references by initialing the Form PTO-1449 attached thereto, and forwarding a copy of the initialed Form to Applicant.

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Withdrawal of the Rejection Under 35 U.S.C. 102(b) is Appropriate and is Respectfully Requested.

Claims 1-21, 23-45, 52, and 57 were rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 5,191,645 to Carlucci et al. (referred to below as "Carlucci"). Applicant respectfully traverses this rejection for the following reasons.

As amended, independent claim 1 recites a video output system for producing video signals within a video graphics workstation. The video output system comprises: a receiver for receiving a video signal forwarded from a video signal source within the video graphics workstation; a video pipeline for post-processing the received video signal, the video pipeline producing a post-processed video signal; and a video output module for converting the post-processed video signal, the video output module producing a formatted video signal. The claim has been amended to recite that: the video output system is selectively coupled to a storage medium as one video signal source; the video output system is selectively coupled to a video graphics processor as another video signal source; and the video output system is selectively coupled to a video input system as another video signal source. Fig. 1 of the instant specification shows one exemplary and non-limiting system encompassed by the subject matter recited in claim 1. The workstation 100 shown in that figure includes a video output system 140 that is selectively coupled to a video graphics processor 130, a storage medium 120, and a video input system 110. One exemplary benefit of the design strategy shown in Fig. 1 is its scalability, which enables it to adapt to the changing needs of the workstation's user.

Carlucci does not teach or suggest the above-described recitations of claim 1. As the prior Response of May 21, 2003 provided a general description of the Carlucci reference, this Response will directly advance to an explanation of the deficiencies of the Carlucci reference vis-à-vis the rejected claims.

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The Office Action now interprets the claimed "receiver" as Carlucci's input processor 70, the claimed "video pipeline" as Carlucci's digital signal processor 72, and the claimed "video output module" as Carlucci's output processor 74. In an apparent alternative interpretation, the Office Action also interprets the claimed "receiver" as Carlucci's filter 100 of the input processor 70, the claimed "video pipeline" as Carlucci's A/D converter 102 of the input processor 70, and the claimed "video output module" as Carlucci's frame buffers (104, 106, 108) of the input processor 70. However, this interpretation is misplaced because Carlucci pertains to a markedly different system than the invention recited in claim 1. For instance, claim 1 recites that the receiver of the video output system receives a "video signal forwarded from a video signal source within the video graphics workstation." Further, the claim now explicitly states that the video output system is selectively coupled to: a storage medium as one video signal source; a video graphics processor as another video signal source; and a video input system as another video signal source. Carlucci does not disclose at least the above-described X features. For instance, Carlucci's input processor 70 (which is being interpreted by the Office Action as the recited "receiver") receives an analog input signal only from a camera processor 12. Camera processor 12, in turn, functions by imaging film frames adjacent to it (column 3, lines 51-53 of Carlucci). Accordingly, the input processor 70 is clearly not selectively coupled to a storage medium, video graphics processor, and a X video signal source. For instance, the camera processor 12 functions neither as a storage medium nor as a video graphics processor. As mentioned above, the Patent Office has alternatively interpreted the claimed "receiver" as the filter 100 (shown in Fig. 3) of the input processor 70. Again, however, the filter 100 is clearly not selectively coupled to a storage medium, video graphics processor, and a video signal source; it is only coupled to the camera processor 12.

In addressing the above-noted features of claim 1 (formally recited in withdrawn claims 6-8), the Office Action also generally directs the Applicant's attention to Figs. 3 and 4 of Carlucci. These figures show two different respective implementations of the input processor 70. Since the input processor 70 is itself what is being construed as the claimed "receiver," these figures do not show any components that function as a source of information that is fed to the receiver. For instance, it is true that these figures show memory (e.g., buffers 104, 106, 108). But this memory does not constitute a storage medium or a video graphics processor that feeds information to the receiver of the video output system (which the Office Action is interpreting as Carlucci's input processor 70 and/or the filter 100 of the input processor 70). In other words, the output of the buffers (104, 106, 108) is not fed to the input of the input processor 70 or filter 100.

A claim is properly rejected under 35 U.S.C. § 102 only if each and every element of the claim is found in the cited reference. For the above-identified reasons, Clarlucci does not disclose each and every element of claim 1 – and quite to the contrary, describes a markedly different system than the invention recited in claim 1. Accordingly, the Applicant respectively requests that the rejection of claim 1 under 35 U.S.C. § 102 be withdrawn. Independent claims 23, 44, 52, and 57 recite related subject matter to claim 1, and are therefore allowable for similar reasons to those specified above.

The remaining claims rejected under 35 U.S.C. § 102(b) (i.e., claims 2-5, 9-21, 24-27, 31-43, and 45) depend variously from claims 1, 23, and 44, and are therefore allowable for at least this reason. In addition, these claims recite additional features that are not taught or suggested by Carlucci. For instance, claims 10-21 recite various functions involved in the process of post-processing performed by the claimed "video pipeline." Again, the Office Action is interpreting the claimed "video pipeline" as either Carlucci's digital signal processor 72 or Carlucci's A/D converter 102. Neither of these

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components performs all of the functions identified in claims 10-21. For instance, with respect to claim 10, the Office Action identifies column 12, lines 32-38 of Carlucci as disclosing region of interest selection (page 3, lines 16 and 17 of the Office Action). Even if, assuming *arguendo*, that this passage describes the subject matter of claim 10, this passage is describing the functionality of the output processor 74, not the digital signal processor 72 (again, which the Office Action is interpreting as the claimed "video pipeline"). With respect to claim 11, the Office Action identifies column 6, lines 25-37 of Carlucci as disclosing frame rate matching (page 3, lines 18-21 of the Office Action). However, that portion is describing functionality of the input processor 70, not the digital signal processor 72. These are merely two illustrations of the deficiencies of the Carlucci reference vis-à-vis the claimed invention.

For at least the above-stated reasons, the Applicant submits that the 35 U.S.C. § 102(b) rejection based on Carlucci is misplaced, and respectfully requests that it be withdraw.

Withdrawal of the Rejections Under 35 U.S.C. § 103 is Appropriate and is Respectfully Requested.

Claim 22 was rejected under 35 U.S.C. § 103 as being unpatentable over Carlucci in view of U.S. Patent No. 5,937,173 to Olarig et al. (referred to below as "Olarig"). Applicant respectfully traverses this rejection for the following reasons.

Claim 22 depends on independent claim 1, and is therefore distinguishable over Carlucci for at least this reason. Olarig also does not disclose the subject matter of independent claim 1 identified above. Therefore, Olarig does not make up for the deficiencies of Carlucci, whether considered alone or in combination with Carlucci. The

Applicant therefore respectfully requests that the U.S.C. § 103 rejection based Carlucci and Olarig be withdrawn.

Claims 46, 47, 49, and 50 were rejected under 35 U.S.C. § 103 as being unpatentable over Carlucci in view of U.S. Patent No. 5,734,589 to Kostreski et al. (referred to below as "Kostreski"). Applicant respectfully traverses this rejection for the following reasons.

Claims 46 and 47 depend on independent claim 1, while claims 49 and 50 depend on independent claim 23. These claims are therefore distinguishable over Carlucci for at least this reason. Kostreski also does not disclose the subject matter of independent claims 1 and 23 identified above. Therefore, Kostreski does not make up for the deficiencies of Carlucci, whether considered alone or in combination with Carlucci.

Kostreski also fails to disclose the subject matter recited in claims 46, 47, 49, and 50, whether considered alone or in combination with Carlucci. For instance, claim 46 recites that the receiver and the video pipeline are implemented as an integrated video processing module, and that the video output module is detachably coupled to the video processing module. Claim 47 specifies that the video output module is a daughterboard module that couples to the video processing module. As referenced in the Office Action, Kostreski discloses a network interface module 101 that takes the form of a plug-in module, similar to a daughterboard or option card which can be plugged into a back plane of a personal computer (column 9, lines 1-4 of Kostreski). While Applicant certainly does not contend that daughterboards are novel *per se*, the use of a daughterboard arrangement in the context of the claimed invention is non-obvious over the applied references. For instance, the Office Action is interpreting Carlucci's output processor 74 as the claimed "video output module." Carclucci's output processor 74 is shown in Fig. 2 as being a functional component within a film scanning processor 14. There is no

suggestion in Carlucci that it would have been desirable or obvious to make any component within the film scanning processor 14 detachable from the remainder of the film scanning processor 14. Further, Kostreski's network interface module 101 provides physical connection to a particular type of network, and therefore is not relevant to the film processing technical environment associated with Carlucci's film scanning processor 14.

The basis on which claims 48 and 51 are rejected is not clear from the Office Action. It is assumed that the Patent Office intended to reject these claims based on the combination of Carlucci and Kostreski. With respect to these claims, the Office Action states that "it is well known in the art to inform of a configuration of any detachable module to the main processor (CPU common on the mother board) so as to make and utilize an operational piece of hardware/ software" (page 6, second paragraph of the Office Action). The Applicant traverses this rejection because there is no support in the record for the conclusion that the identified features are "well known." In accordance with MPEP § 2144.03, the Examiner must cite a reference in support of this Office Action's position.

Likewise, the basis on which claims 53-56 and 58-61 are being rejected is not clear. Presumably, these claims are being rejected under 35 U.S.C. § 103 based on Carlucci. Insofar as these claims recite subject matter found in prior claims, the Applicant respectfully traverses this rejection for the reasons provided above. Moreover, the rejection of a claim such as 54 highlights the overreaching nature of the Office Action. This claim specifies that the "video pipeline" includes functionality for performing no fewer than nine functions specified in claim 53. The Office Action is interpreting the claimed "video pipeline" as Carlucci's digital signal processor 72. This component, as shown in Fig. 5 of Carlucci, includes three functional modules: noise

reducer 80; color processor 82; and image enhancer 84 – not the nine functions recited in claim 53.

For at least the reasons specified above, the Applicant respectfully requests that the U.S.C. § 103 rejection based Carlucci and Kostreski be withdrawn.

Conclusion

In conclusion, the Applicant respectfully requests that the rejections based on 35 U.S.C. § 102 and 35 U.S.C. § 103 be withdrawn.

As a final matter, to clarify the record, the arguments presented above are not exhaustive; Applicant reserves the right to present additional arguments to fortify its position. Further, Applicant reserves the right to challenge the prior art status of one or more documents cited in the Office Action.

All objections and rejections raised in the Office Action having been addressed, it is respectfully submitted that the present application is in condition for allowance and such allowance is respectfully solicited. The Examiner is urged to contact the undersigned if any issues remain unresolved by this Amendment.

By:

Respectfully Submitted,

Dated: August 8, 2003

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